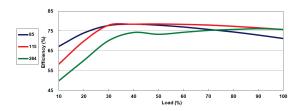


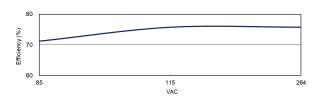
Characteristic Curves

TMP 30105 TMP 30105C

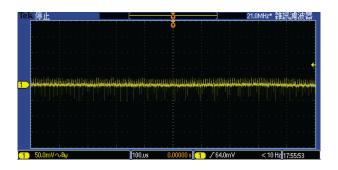
Efficiency vs Output Load



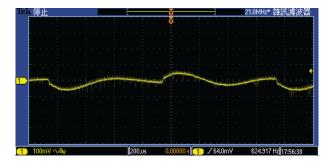
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

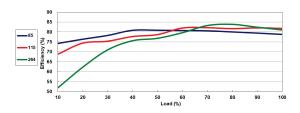


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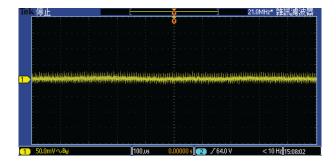


TMP 30112 TMP 30112C

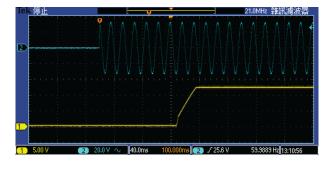
Efficiency vs Output Load



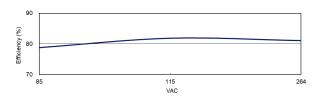
Typical Output Ripple and Noise



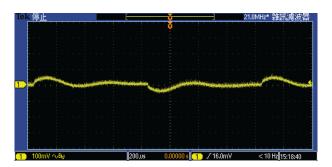
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

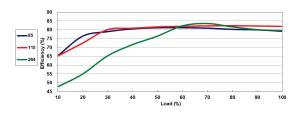


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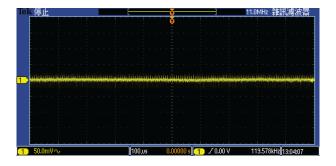


TMP 30115 TMP 30115C

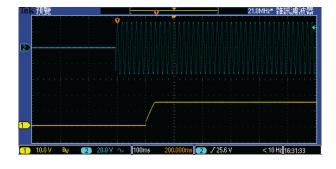
Efficiency vs Output Load



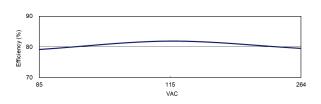
Typical Output Ripple and Noise



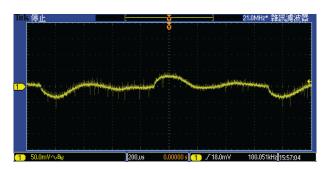
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

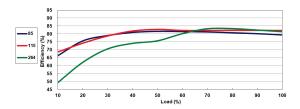


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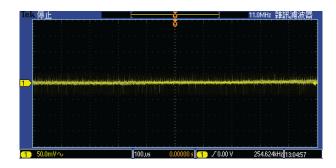


TMP 30124 TMP 30124C

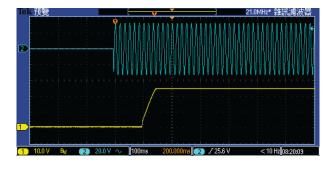
Efficiency vs Output Load



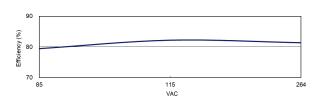
Typical Output Ripple and Noise



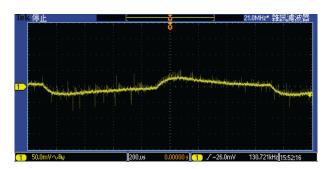
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

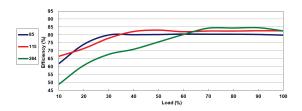


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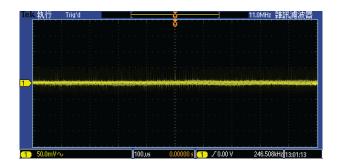


TMP 30148 TMP 30148C

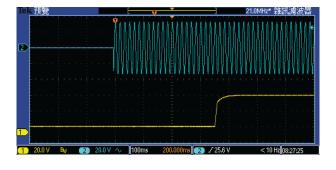
Efficiency vs Output Load



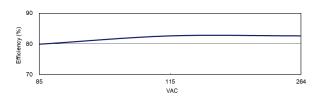
Typical Output Ripple and Noise



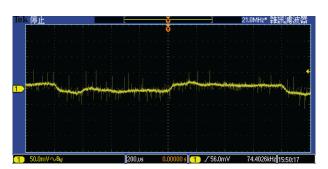
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

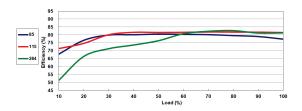


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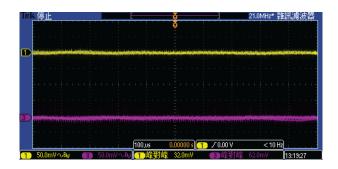


TMP 30212 TMP 30212C

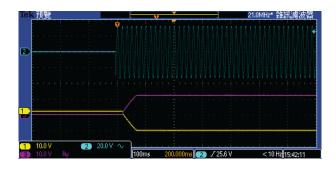
Efficiency vs Output Load



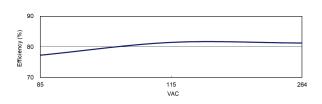
Typical Output Ripple and Noise



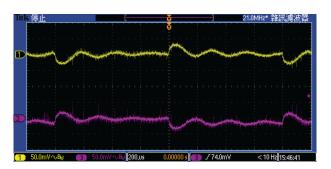
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

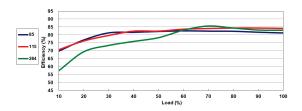


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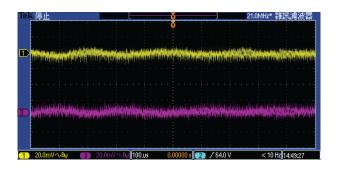


TMP 30215 TMP 30215C

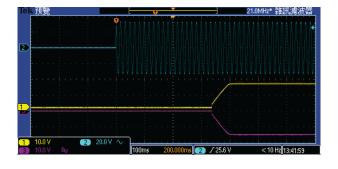
Efficiency vs Output Load



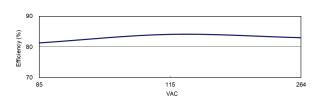
Typical Output Ripple and Noise



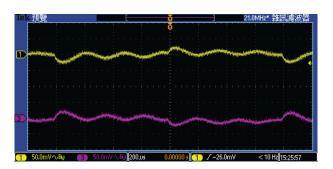
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

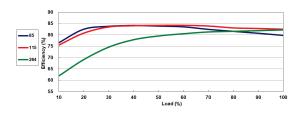


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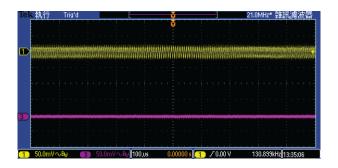


TMP 30252 TMP 30252C

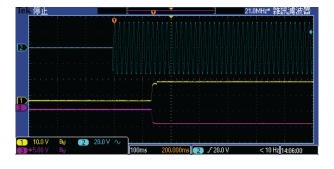
Efficiency vs Output Load



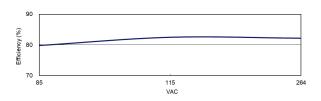
Typical Output Ripple and Noise



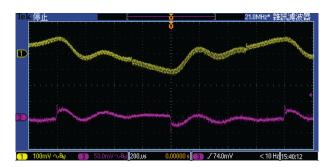
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

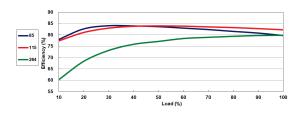


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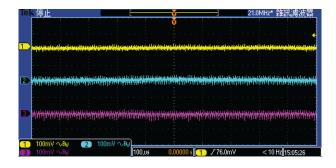


TMP 30512 TMP 30512C

Efficiency vs Output Load



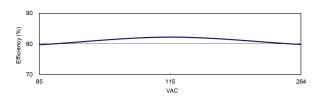
Typical Output Ripple and Noise



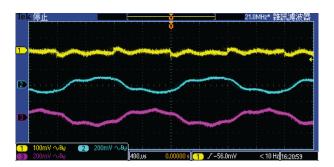
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

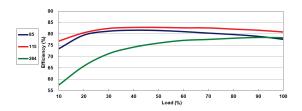


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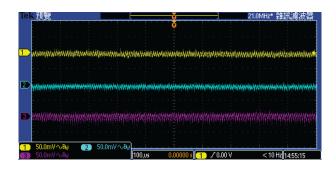


TMP 30515 TMP 30515C

Efficiency vs Output Load



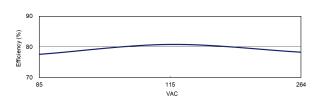
Typical Output Ripple and Noise



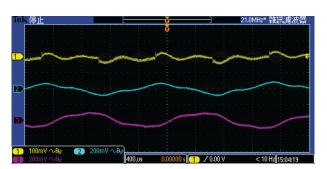
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

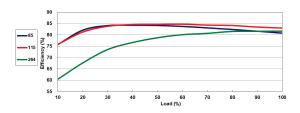


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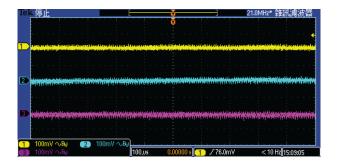


TMP 30522 TMP 30522C

Efficiency vs Output Load



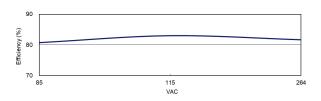
Typical Output Ripple and Noise



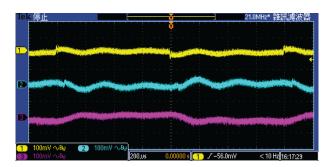
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

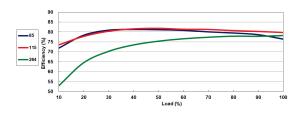


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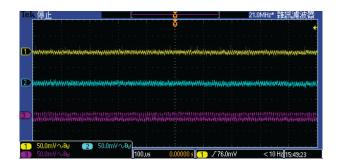


TMP 30316 TMP 30316C

Efficiency vs Output Load



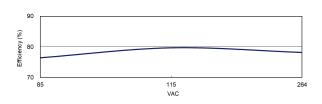
Typical Output Ripple and Noise



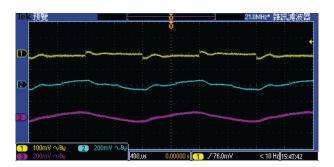
Typical Input Start-Up and Output Rise Characteristic



Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

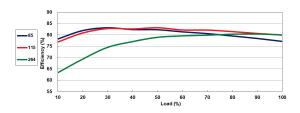


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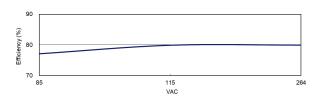


TMP 30317 TMP 30317C

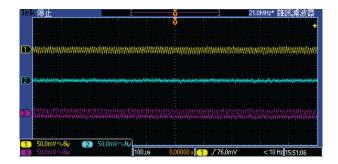
Efficiency vs Output Load



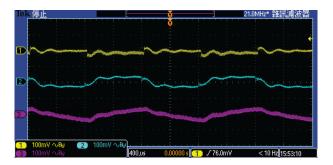
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

